

Marius Strobl

■ NetApp

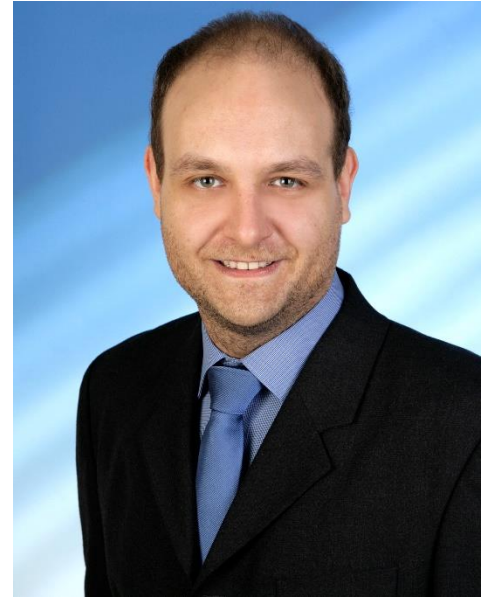
- Research Fellow – Member of Technical Staff
- SSICLOPS Project
- (Kernel Bypass) Networking

■ genua

- Research Engineer
- SIBASE Project
- Secure Embedded Systems (OpenBSD on L4/Fiasco.OC on ARM)

■ University of Applied Sciences Regensburg

- Scientific Faculty Member
- SECBIT Project
- Ethernet over Powerline Communication, Power over Automotive Ethernet
- Virtualization for Reliable Embedded Systems



Open Questions

Research in industry – does it work anywhere/for anyone?

Is academia any better?